

PATENT COOPERATION TREATY

PCT

NOTIFICATION OF ELECTION

(PCT Rule 61.2)

From the INTERNATIONAL BUREAU

To:

United States Patent and Trademark
Office
(Box PCT)
Crystal Plaza 2
Washington, DC 20231
ÉTATS-UNIS D'AMÉRIQUE

in its capacity as elected Office

Date of mailing (day/month/year)

05 February 1999 (05.02.99)

International application No.

PCT/GB98/01775

Applicant's or agent's file reference

IPD/P2613/WOD

International filing date (day/month/year)

17 June 1998 (17.06.98)

Priority date (day/month/year)

25 June 1997 (25.06.97)

Applicant

ALLENSON, Michael, Barry et al

1. The designated Office is hereby notified of its election made:



in the demand filed with the International Preliminary Examining Authority on:

21 January 1999 (21.01.99)



in a notice effecting later election filed with the International Bureau on:

2. The election ☒ was

was not

made before the expiration of 19 months from the priority date or, where Rule 32 applies, within the time limit under Rule 32.2(b).

The International Bureau of WIPO
34, chemin des Colombettes
1211 Geneva 20, Switzerland

Facsimile No.: (41-22) 740.14.35

Authorized officer

Marie-José Devillard

Telephone No.: (41-22) 338.83.38

Copy for the Elected Office (EO/US)

PATENT COOPERATION TREATY

PCT/GB98/01775

PCT

**NOTIFICATION OF THE RECORDING
OF A CHANGE**

(PCT Rule 92bis.1 and
Administrative Instructions, Section 422)

From the INTERNATIONAL BUREAU

To:

BOWDERY, Anthony, Oliver
D/IPD DERA Formalities
A4 Building, Room G016
Ively Road
Farnborough
Hants GU14 0LX
ROYAUME-UNI

Date of mailing (day/month/year) 14 September 1999 (14.09.99)	IMPORTANT NOTIFICATION
Applicant's or agent's file reference IPD/P2613/WOD	
International application No. PCT/GB98/01775	International filing date (day/month/year) 17 June 1998 (17.06.98)

1. The following indications appeared on record concerning: <input type="checkbox"/> the applicant <input type="checkbox"/> the inventor <input checked="" type="checkbox"/> the agent <input type="checkbox"/> the common representative									
Name and Address BOWDERY, Anthony, Oliver D/IPR Formalities Section Poplar 2 MOD (PE) Abbey Wood #19 Bristol BS34 8JH United Kingdom	<table border="1"> <tr> <td>State of Nationality</td> <td>State of Residence</td> </tr> <tr> <td colspan="2">Telephone No. 0117 91 32857</td> </tr> <tr> <td colspan="2">Facsimile No. 0117 91 32930</td> </tr> <tr> <td colspan="2">Teleprinter No.</td> </tr> </table>	State of Nationality	State of Residence	Telephone No. 0117 91 32857		Facsimile No. 0117 91 32930		Teleprinter No.	
State of Nationality	State of Residence								
Telephone No. 0117 91 32857									
Facsimile No. 0117 91 32930									
Teleprinter No.									
2. The International Bureau hereby notifies the applicant that the following change has been recorded concerning: <input type="checkbox"/> the person <input type="checkbox"/> the name <input checked="" type="checkbox"/> the address <input type="checkbox"/> the nationality <input type="checkbox"/> the residence									
Name and Address BOWDERY, Anthony, Oliver D/IPD DERA Formalities A4 Building, Room G016 Ively Road Farnborough Hants GU14 0LX United Kingdom	<table border="1"> <tr> <td>State of Nationality</td> <td>State of Residence</td> </tr> <tr> <td colspan="2">Telephone No. 01252 392 710</td> </tr> <tr> <td colspan="2">Facsimile No. 01252 393 920</td> </tr> <tr> <td colspan="2">Teleprinter No.</td> </tr> </table>	State of Nationality	State of Residence	Telephone No. 01252 392 710		Facsimile No. 01252 393 920		Teleprinter No.	
State of Nationality	State of Residence								
Telephone No. 01252 392 710									
Facsimile No. 01252 393 920									
Teleprinter No.									
3. Further observations, if necessary: 									
4. A copy of this notification has been sent to: <table border="0"> <tr> <td><input checked="" type="checkbox"/> the receiving Office</td> <td><input type="checkbox"/> the designated Offices concerned</td> </tr> <tr> <td><input type="checkbox"/> the International Searching Authority</td> <td><input checked="" type="checkbox"/> the elected Offices concerned</td> </tr> <tr> <td><input checked="" type="checkbox"/> the International Preliminary Examining Authority</td> <td><input type="checkbox"/> other:</td> </tr> </table>		<input checked="" type="checkbox"/> the receiving Office	<input type="checkbox"/> the designated Offices concerned	<input type="checkbox"/> the International Searching Authority	<input checked="" type="checkbox"/> the elected Offices concerned	<input checked="" type="checkbox"/> the International Preliminary Examining Authority	<input type="checkbox"/> other:		
<input checked="" type="checkbox"/> the receiving Office	<input type="checkbox"/> the designated Offices concerned								
<input type="checkbox"/> the International Searching Authority	<input checked="" type="checkbox"/> the elected Offices concerned								
<input checked="" type="checkbox"/> the International Preliminary Examining Authority	<input type="checkbox"/> other:								

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland Facsimile No.: (41-22) 740.14.35	Authorized officer Ting Zhao Telephone No.: (41-22) 338.83.38
--	--

PATENT COOPERATION TREATY

REC'D 01 NOV 1999

PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference IPD/P2613/WOD		See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416) FOR FURTHER ACTION	
International application No. PCT/GB98/01775	International filing date (day/month/year) 17/06/1998	Priority date (day/month/year) 25/06/1997	
International Patent Classification (IPC) or national classification and IPC H01S3/25			
Applicant THE SECRETARY OF STATE FOR DEFENCE et al.			

1. This international preliminary examination report has been prepared by this International Preliminary Examining Authority and is transmitted to the applicant according to Article 36.



2. This REPORT consists of a total of 6 sheets, including this cover sheet.

- ☒ This report is also accompanied by ANNEXES, i.e. sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications made before this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions under the PCT).

These annexes consist of a total of 7 sheets.

3. This report contains indications relating to the following items:

- I ☒ Basis of the report
- II ☐ Priority
- III ☐ Non-establishment of opinion with regard to novelty, inventive step and industrial applicability
- IV ☐ Lack of unity of invention
- V ☒ Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement
- VI ☐ Certain documents cited
- VII ☐ Certain defects in the international application
- VIII ☒ Certain observations on the international application

Date of submission of the demand 21/01/1999	Date of completion of this report 28. 10. 99
Name and mailing address of the international preliminary examining authority:  European Patent Office D-80298 Munich Tel. +49 89 2399 - 0 Tx: 523656 epmu d Fax: +49 89 2399 - 4465	Authorized officer Moskowitz, P Telephone No. +49 89 2399 2521 

**INTERNATIONAL PRELIMINARY
EXAMINATION REPORT**

International application No. PCT/GB98/01775

I. Basis of the report

1. This report has been drawn on the basis of (*substitute sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to the report since they do not contain amendments.*):

Description, pages:

1-3,7-17 as originally filed

4-6 as received on 09/07/1999 with letter of 05/07/1999

Claims, No.:

1-17 as received on 09/07/1999 with letter of 05/07/1999

Drawings, sheets:

1/6-6/6 as originally filed

2. The amendments have resulted in the cancellation of:

- ☐ the description, pages:
☐ the claims, Nos.:
☐ the drawings, sheets:

3. ☐ This report has been established as if (some of) the amendments had not been made, since they have been considered to go beyond the disclosure as filed (Rule 70.2(c)):

4. Additional observations, if necessary:

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No. PCT/GB98/01775

V. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1. Statement

Novelty (N)	Yes: Claims 1-17
	No: Claims
Inventive step (IS)	Yes: Claims 1-17
	No: Claims
Industrial applicability (IA)	Yes: Claims 1-17
	No: Claims

2. Citations and explanations

see separate sheet

VIII. Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made:

see separate sheet

**INTERNATIONAL PRELIMINARY
EXAMINATION REPORT - SEPARATE SHEET**

International application No. PCT/GB98/01775

V.

1. Reference is made to the following documents:

D1: WO-A-96 08857.

D2: WO-A-95 07565.

D3: WO-A-92 15136.

As the priority document was not available to the Examiner at the time of writing this report, it is being assumed that the priority date is valid for all claims. If it were later to turn out, however, that the priority date is not valid for all or some of the claims, the **D.R. Wight et.al. document** cited as "P,X" in the international search report would become relevant as prior art.

- 2.1 Each of documents D1, D2 and D3 discloses a light emitting device having a plurality of light emitting means electrically connected in series. In each of these documents the light emitting means are also arranged optically such that they share a common optical waveguide.

The subject-matter of independent claim 1 differs from that of any of documents D1, D2 or D3 in that in claim 1 the light emitting means are arranged optically such that they **do not** share a common optical waveguide, for generating **two or more** beams of output radiation.

In D2, the plurality of laser structures are arranged in one and the same optical cavity in the optical direction of propagation, so as to share a common waveguide. Only two output beams can be emitted, one from either end of the device.

In D3, a common active region (14), or alternatively a common passive waveguide (15), extends continuously through the plurality of laser sections, such that they share a common waveguide. A single output beam only is emitted.

In D1, it is not specified exactly how the individual diodes are *optically* coupled. However, it appears that only one output beam is emitted, so that it would appear that they are arranged optically to share a common optical waveguide. In any case, there is no suggestion in D1 of arranging the diodes optically such that the light emitting means **do not** share a common optical waveguide, especially in light of the teachings of D2 and D3.

Therefore, the subject-matter of claim 1 of the present application is both novel (Article 33(2) PCT) and involves an inventive step (Article 33(3) PCT).

2.2 Claims 2-13 and 16,17 are dependent on claim 1 and as such also meet the requirements of the PCT with respect to novelty and inventive step.

2.3 Furthermore, the methods defined in independent claims 14 and 15 make use of the light emitting device of claim 1, and as such also meet the requirements of the PCT with respect to novelty and inventive step.

VIII.

1. Claim 2, in combination with claim 1, does not meet the requirements of Article 6 PCT, as the subject-matter of claim 2 is in direct contradiction with that of claim 1 to which it refers. This inconsistency between the claims leads to doubt concerning the matter for which protection is sought, thereby rendering the claims unclear (Article 6 PCT).

More specifically, claim 1 requires that the light emitting means are electrically connected **in series**. However, dependent claim 2 states that the light emitting means are electrically connected in a **parallel** connection or in a **series parallel** connection.

**INTERNATIONAL PRELIMINARY
EXAMINATION REPORT - SEPARATE SHEET**

International application No. PCT/GB98/01775

Correspondingly, the embodiments in the description which do not fall within the scope of newly filed claim 1 (i.e. in which the light emitting means are electrically connected in a **parallel** connection or in a **series parallel** connection) create an inconsistency between the claims and the description, leading to doubt concerning the matter for which protection is sought, thereby rendering the claims unclear (Article 6 PCT).

According to one aspect of the invention, there is provided a light emitting device, having an input impedance and a device quantum efficiency, for generating two or more beams of output radiation from an input current of electrons comprising;

at least two light emitting means for converting the input current of electrons into a beam of output radiation, each of the light emitting means having a respective optical waveguide and having an impedance and an individual quantum efficiency,

wherein the light emitting means (i) are electrically connected in series such that the input impedance of the light emitting device is substantially equal to the sum of the impedances of the light emitting means and such that the quantum efficiency of the device is substantially equal to the sum of the individual quantum efficiencies of the light emitting means and (ii) are arranged optically such that the light emitting means do not share a common optical waveguide.

The light emitting means may be electrically connected in any one of a parallel connection or in a series parallel connection.

In a preferred embodiment, the light emitting means may be electrically connected such that the input impedance of the light emitting device is substantially equal to 50 Ω without additional circuitry or impedance matching elements.

The light emitting means, having a modulation frequency limit, wherein the input impedance of the light emitting device is substantially equal to 50 Ω across a frequency range substantially from DC to the modulation frequency limit of each of the light emitting means.

The light emitting means may be p-n junctions, for example laser diodes or light emitting diodes. Typically, the laser diode devices may be any one of AlGaAs, AlGaInP, AlGaInAs or AlGaInAsP laser diode devices. The p-n junctions may have an end face coated with a reflective coating.

According to another aspect of the invention, there is provided an optically coupled transistor for generating an output electrical signal comprising;

the light emitting device as herein described for emitting at least two beams of output radiation and

at least one photodetector for detecting the beams of radiation output from the light emitting device and for converting the beams of output radiation into an output electrical current,

wherein the light emitting device and the at least one photodetector are arranged such that there is no electrical feedback from the at least one photodetector to the light emitting device.

In one embodiment of the optically coupled transistor, the one or more photodetectors may be a photodiode device

For example, the optically coupled transistor may comprise at least two photodetectors, wherein the photodetectors are connected in any one of a series connection, a parallel connection or a series parallel connection.

The optically coupled transistor may also comprise one or more optical fibres for transmitting the beams of output radiation to the one or more photodetectors.

According to another aspect of the invention, a fibre optic link comprises one or more optical fibres having an input endface and an output endface, and also comprising the light emitting device as herein described, wherein the light emitting device is situated at the input endface of one or more optical fibres such that the beams of radiation output from the light emitting device are input to the one or more optical fibres.

According to another aspect of the invention, there is provided a method for distributing an input signal into an output channel comprising the steps of;

- (i) outputting two or more beams of radiation from the light emitting device as herein described, comprising at least two light emitting means and
- (ii) inputting the two or more beams of output radiation into the output channel.

According to another aspect of the invention, there is provided a method for distributing an input signal into a plurality of output channels comprising the steps of;

- (i) outputting two or more beams of radiation from the light emitting device as herein described, comprising at least two light emitting means and
- (ii) inputting each of the two or more beams of output radiation into a different one of the output channels.

According to another aspect of the invention, an optical repeater for receiving an optical input signal and generating one or more optical output signals comprises;

a photodetector for receiving the optical input signal and converting the optical input signal into an electrical signal and

the light emitting device as herein described for receiving the said electrical signal and outputting one or more optical signals.

In a preferred embodiment, the optical repeater also comprises amplification means for amplifying the electrical signal output from the photodetector.

AMENDED SHEET

Claims

*112p2

1. A light emitting device (1), having an input impedance and a device quantum efficiency, for generating two or more beams of output radiation from an input current of electrons comprising:

at least two light emitting means (2a.2b) for converting the input current of electrons into a beam of output radiation, each of the light emitting means having a respective optical waveguide and having an impedance and an individual quantum efficiency, *

wherein the light emitting means (2a.2b) (i) are electrically connected in series such that the input impedance of the light emitting device (1) is substantially equal to the sum of the impedances of the light emitting means (2a.2b) and such that the quantum efficiency of the device (1) is substantially equal to the sum of the individual quantum efficiencies of the light emitting means and (ii) are arranged optically such that the light emitting means do not share a common optical waveguide.

2. The light emitting device (1) of Claim 1 wherein the light emitting means (2a.2b) are electrically connected in any one of a parallel connection or in a series parallel connection.

3. The light emitting device of Claim 1 or Claim 2, wherein the light emitting means (2a.2b) are electrically connected such that the input impedance of the light emitting device is substantially equal to $50\ \Omega$ without additional circuitry or impedance matching elements.

4. The light emitting device of Claim 3, wherein each of the light emitting means (2a.2b) have a modulation frequency limit and wherein the input impedance of the light emitting device (1) is substantially equal to $50\ \Omega$ across a frequency range substantially from DC to the modulation frequency limit of each of the light emitting means (2a.2b).

5. The light emitting device (1) of Claim 1 wherein the light emitting means (2a.2b) are p-n junctions (4.5.6).

6. The light emitting device (1) of Claim 5, wherein the p-n junctions (4.5.6) are laser diodes or light emitting diodes.

7. The light emitting device (1) of Claim 6, wherein the laser diode devices may be any one of AlGaAs, AlGaInP, AlGaInAs or AlGaInAsP laser diode devices.

8. The light emitting device (1) of Claim 6, wherein the p-n junctions (4.5.6) each have an end face coated with a reflective coating.

* 9. An optically coupled transistor (18) for generating an output electrical signal comprising;

the light emitting device (1;21) of any of Claims 1-4 for emitting at least two beams of output radiation (29) and

at least one photodetector (23) for detecting the beams of radiation output (29) from the light emitting device (1) and for converting the beams of output radiation (29) into an output electrical current (Ic).

wherein the light emitting device (1) and the at least one photodetector (23) are arranged such that there is no electrical feedback from the at least one photodetector (23) to the light emitting device (1).

10. The optically coupled transistor (18) of Claim 9 wherein the one or more photodetector is a photodiode device (23).

11. The optically coupled transistor (18) of Claim 9, comprising at least two photodetectors, wherein the photodetectors are connected in any one of a series connection, a parallel connection or a series parallel connection.

12. The optically coupled transistor (18) of Claim 9, comprising one or more optical fibres for transmitting the beams of output radiation (29) to the one or more photodetectors.

13. A fibre optic link comprising one or more optical fibres having an input endface and an output endface, and also comprising the light emitting device (1) of Claim 1,

wherein the light emitting device (1) is situated at the input endface of one or more optical fibres such that the beams of radiation output from the light emitting device are input to the one or more optical fibres.

14. A method for distributing an input signal into an output channel comprising the steps of:

(i) outputting two or more beams of radiation from the light emitting device (1) of Claim 1 comprising at least two light emitting means (2a.2b:21) and

(ii) inputting the two or more beams of output radiation into the output channel.

whereby the light emitting means (2a.2b:21) are connected such that the device quantum efficiency is greater than or equal to the individual quantum efficiency of one of the light emitting means.

15. A method for distributing an input signal into a plurality of output channels comprising the steps of:

(i) outputting two or more beams of radiation from the light emitting device (1) of Claim 1 comprising at least two light emitting means (2a.2b:21) and

(ii) inputting each of the two or more beams of output radiation into a different one of the output channels.

whereby the light emitting means (2a,2b,21) are connected such that the device quantum efficiency is greater than or equal to the individual quantum efficiency of one of the light emitting means.

16. An optical repeater for receiving an optical input signal and generating one or more optical output signals comprising:

a photodetector (23) for receiving the optical input signal and converting the optical input signal into an electrical signal and

the light emitting device (1) of any of Claims 1-4 for receiving the electrical signal and outputting one or more optical signals.

17. The optical repeater of Claim 16 and also comprising amplification means for amplifying the electrical signal output from the photodetector (23).

INTERNATIONAL SEARCH REPORT

Intel. Application No

PCT/GB 98/01775

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H01S3/25 H04B10/16 H03F3/08 H04B10/12 H04B10/14

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01S H04B H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y A	WO 96 08857 A (EDWARDS PAUL JULIAN ;CHEUNG WOOD NANG (AU)) 21 March 1996 see the whole document	1-3, 6-8, 10-12 1, 14-18 4, 5, 9
X A	WO 95 07565 A (ERICSSON TELEFON AB L M) 16 March 1995 see page 3, line 9-28 see page 6, line 1-13 see page 8, line 1-9	1-5 10, 14, 15, 17
X A	WO 92 15136 A (UNIV MELBOURNE) 3 September 1992 see page 2	1-5 10, 14, 15, 17
	--- -/-	



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

* Special categories of cited documents:

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

16 October 1998

Date of mailing of the international search report

30/10/1998

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Claessen, L

INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 98/01775

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	PATENT ABSTRACTS OF JAPAN vol. 009, no. 015 (E-291), 22 January 1985 & JP 59 163927 A (SUMITOMO DENKI KOGYO KK), 17 September 1984 see abstract	1, 14, 15
A	PATENT ABSTRACTS OF JAPAN vol. 011, no. 382 (E-564), 12 December 1987 & JP 62 146024 A (SUMITOMO ELECTRIC IND LTD), 30 June 1987 see abstract	1, 14, 15
Y	US 4 644 587 A (TAKAHASHI YOSHIFUSA ET AL) 17 February 1987 see abstract; figure 5	1, 15-18
A	PATENT ABSTRACTS OF JAPAN vol. 014, no. 564 (E-1013), 14 December 1990 & JP 02 244834 A (FUJITSU LTD), 28 September 1990 see abstract	1, 15
A	US 4 212 023 A (CHEN ROBERT I ET AL) 8 July 1980 see the whole document	1, 10
A	US 4 278 515 A (PEARLSTEIN FRED ET AL) 14 July 1981 see the whole document	1, 10
A	US 5 278 515 A (MATHEWS KURK D) 11 January 1994 see the whole document	1
P, X	D.R. WIGHT ET AL: "Optically coupled transistors and the achievement of insertion gain in broadband directly modulated optical links" IEE COLLOQUIUM ON FIBRE OPTICS IN MICROWAVE SYSTEMS AND RADIO ACCESS (REF. NO. 1997/131, 26 June 1997, page 5/1-5 XP002081085 birmingham uk see the whole document	1-18

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/GB 98/01775

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
WO 9608857	A	21-03-1996	AU 682982 B	23-10-1997
			AU 3514495 A	29-03-1996
			JP 10505201 T	19-05-1998
WO 9507565	A	16-03-1995	SE 501721 C	02-05-1995
			CA 2169439 A	16-03-1995
			EP 0717884 A	26-06-1996
			JP 9502307 T	04-03-1997
			SE 9302951 A	11-03-1995
			US 5568498 A	22-10-1996
WO 9215136	A	03-09-1992	AU 636792 B	06-05-1993
			AT 137893 T	15-05-1996
			AU 8343991 A	20-08-1992
			CA 2103720 A	14-08-1992
			DE 69210589 D	13-06-1996
			DE 69210589 T	02-10-1996
			EP 0571476 A	01-12-1993
			JP 6505364 T	16-06-1994
			US 5440577 A	08-08-1995
US 4644587	A	17-02-1987	JP 1299634 C	31-01-1986
			JP 58096436 A	08-06-1983
			JP 59016453 B	16-04-1984
			DE 3244851 A	16-06-1983
			GB 2111337 A,B	29-06-1983
US 4212023	A	08-07-1980	DE 2947267 A	04-06-1980
			JP 1123785 C	30-11-1982
			JP 55102281 A	05-08-1980
			JP 57012546 B	11-03-1982
US 4278515	A	14-07-1981	US 4365481 A	28-12-1982
US 5278515	A	11-01-1994	NONE	

PATENT COOPERATION TREATY

PCT

INTERNATIONAL SEARCH REPORT

(PCT Article 18 and Rules 43 and 44)

Applicant's or agent's file reference IPD/P2613/WOD	FOR FURTHER ACTION see Notification of Transmittal of International Search Report (Form PCT/ISA/220) as well as, where applicable, item 5 below.	
International application No. PCT/GB 98/ 01775	International filing date (day/month/year) 17/06/1998	(Earliest) Priority Date (day/month/year) 25/06/1997
Applicant THE SECRETARY OF STATE FOR DEFENCE et al.		

This International Search Report has been prepared by this International Searching Authority and is transmitted to the applicant according to Article 18. A copy is being transmitted to the International Bureau.

This International Search Report consists of a total of 4 sheets.

☒ It is also accompanied by a copy of each prior art document cited in this report.

1. ☐ **Certain claims were found unsearchable**(see Box I).

2. ☐ **Unity of invention is lacking**(see Box II).

3. ☐ The international application contains disclosure of a **nucleotide and/or amino acid sequence listing** and the international search was carried out on the basis of the sequence listing

☐ filed with the international application.

☐ furnished by the applicant separately from the international application.

☐ but not accompanied by a statement to the effect that it did not include matter going beyond the disclosure in the international application as filed.

☐ Transcribed by this Authority

4. With regard to the **title**, ☒ the text is approved as submitted by the applicant

☐ the text has been established by this Authority to read as follows:

5. With regard to the **abstract**,

☐ the text is approved as submitted by the applicant

☒ the text has been established, according to Rule 38.2(b), by this Authority as it appears in Box III. The applicant may, within one month from the date of mailing of this International Search Report, submit comments to this Authority.

6. The figure of the **drawings** to be published with the abstract is:

Figure No. 6A ☐ as suggested by the applicant.

☐ None of the figures.

☒ because the applicant failed to suggest a figure.

☐ because this figure better characterizes the invention.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/GB 98/ 01775

Box III TEXT OF THE ABSTRACT (Continuation of item 5 of the first sheet)

The abstract is changed as follow:

Line 13: delete from "device" until line 18 "fibre".

INTERNATIONAL SEARCH REPORT

International Application No
PCT/GB 98/01775

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H01S3/25 H04B10/16 H03F3/08 H04B10/12 H04B10/14

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01S H04B H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y A	WO 96 08857 A (EDWARDS PAUL JULIAN ;CHEUNG WOOD NANG (AU)) 21 March 1996 see the whole document ---	1-3, 6-8, 10-12 1, 14-18 4, 5, 9
X A	WO 95 07565 A (ERICSSON TELEFON AB L M) 16 March 1995 see page 3, line 9-28 see page 6, line 1-13 see page 8, line 1-9 ---	1-5 10, 14, 15, 17
X A	WO 92 15136 A (UNIV MELBOURNE) 3 September 1992 see page 2 ---	1-5 10, 14, 15, 17
	-/--	

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

16 October 1998

Date of mailing of the international search report

30/10/1998

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Claessen, L

INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 98/01775

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	PATENT ABSTRACTS OF JAPAN vol. 009, no. 015 (E-291), 22 January 1985 & JP 59 163927 A (SUMITOMO DENKI KOGYO KK), 17 September 1984 see abstract ---	1, 14, 15
A	PATENT ABSTRACTS OF JAPAN vol. 011, no. 382 (E-564), 12 December 1987 & JP 62 146024 A (SUMITOMO ELECTRIC IND LTD), 30 June 1987 see abstract ---	1, 14, 15
Y	US 4 644 587 A (TAKAHASHI YOSHIFUSA ET AL) 17 February 1987 see abstract; figure 5 ---	1, 15-18
A	PATENT ABSTRACTS OF JAPAN vol. 014, no. 564 (E-1013), 14 December 1990 & JP 02 244834 A (FUJITSU LTD), 28 September 1990 see abstract ---	1, 15
A	US 4 212 023 A (CHEN ROBERT I ET AL) 8 July 1980 see the whole document ---	1, 10
A	US 4 278 515 A (PEARLSTEIN FRED ET AL) 14 July 1981 see the whole document ---	1, 10
A	US 5 278 515 A (MATHEWS KURK D) 11 January 1994 see the whole document ---	1
P, X	D.R. WIGHT ET AL: "Optically coupled transistors and the achievement of insertion gain in broadband directly modulated optical links" IEE COLLOQUIUM ON FIBRE OPTICS IN MICROWAVE SYSTEMS AND RADIO ACCES (REF. NO. 1997/131, 26 June 1997, page 5/1-5 XP002081085 birmingham uk see the whole document -----	1-18

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/GB 98/01775

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
WO 9608857	A	21-03-1996	AU 682982 B AU 3514495 A JP 10505201 T	23-10-1997 29-03-1996 19-05-1998
WO 9507565	A	16-03-1995	SE 501721 C CA 2169439 A EP 0717884 A JP 9502307 T SE 9302951 A US 5568498 A	02-05-1995 16-03-1995 26-06-1996 04-03-1997 11-03-1995 22-10-1996
WO 9215136	A	03-09-1992	AU 636792 B AT 137893 T AU 8343991 A CA 2103720 A DE 69210589 D DE 69210589 T EP 0571476 A JP 6505364 T US 5440577 A	06-05-1993 15-05-1996 20-08-1992 14-08-1992 13-06-1996 02-10-1996 01-12-1993 16-06-1994 08-08-1995
US 4644587	A	17-02-1987	JP 1299634 C JP 58096436 A JP 59016453 B DE 3244851 A GB 2111337 A, B	31-01-1986 08-06-1983 16-04-1984 16-06-1983 29-06-1983
US 4212023	A	08-07-1980	DE 2947267 A JP 1123785 C JP 55102281 A JP 57012546 B	04-06-1980 30-11-1982 05-08-1980 11-03-1982
US 4278515	A	14-07-1981	US 4365481 A	28-12-1982
US 5278515	A	11-01-1994	NONE	



Application No: GB 9820373.0
Claims searched: 1-20

Examiner: Miss E.L.Rendle
Date of search: 12 January 1999

Patents Act 1977
Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.Q): H1K (KAAE, KEAP, KED, KFXL, KPADL)

Int Cl (Ed.6): H01L 25/075, 25/16, 25/18, 27/142, 27/15, 31/12, 31/14, 31/147,
31/153, 31/16, 31/167, 31/173; H01S 3/085, 3/25

Other: EPOQUE: WPI, EPODOC, PAJ

Documents considered to be relevant:

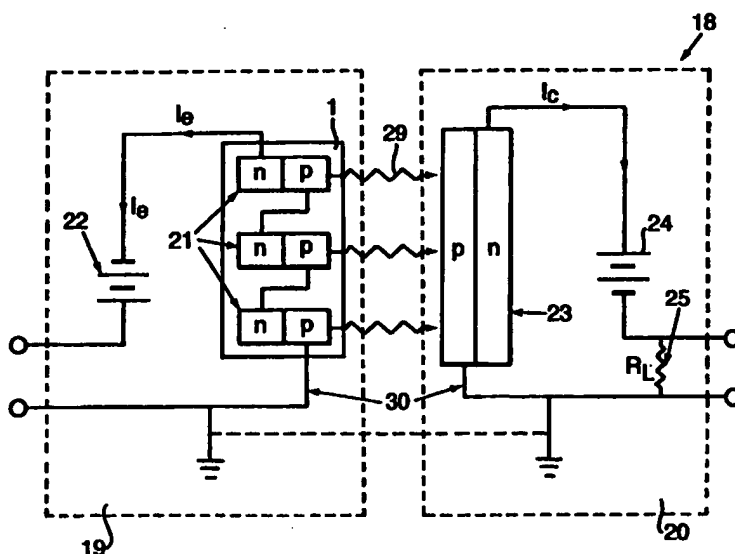
Category	Identity of document and relevant passage	Relevant to claims
X	WO 92/15136 A1 (UNIVERSITY OF MELBOURNE) see whole document, especially figures 1 and 2.	1, 2, 3, 4, 5, 6, 7, 8, 9, 11, 12, 13, 17, 18 20.

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : H01S 3/25, H04B 10/16, H03F 3/08, H04B 10/12, 10/14	A1	(11) International Publication Number: WO 99/00881 (43) International Publication Date: 7 January 1999 (07.01.99)
(21) International Application Number: PCT/GB98/01775 (22) International Filing Date: 17 June 1998 (17.06.98) (30) Priority Data: 9713365.6 25 June 1997 (25.06.97) GB (71) Applicant (for all designated States except US): THE SECRETARY OF STATE FOR DEFENCE [GB/GB]; Defence Evaluation & Research Agency, Ively Road, Farnborough, Hampshire GU14 0LX (GB). (72) Inventors; and (75) Inventors/Applicants (for US only): ALLENSON, Michael, Barry [GB/GB]; (GB). AYING, Stephen, Gerard [GB/GB]; (GB). WIGHT, David, Robert [GB/GB]; DERA Malvern, St. Andrews Road, Malvern, Worcs WR14 3PS (GB). (74) Agent: BOWDERY, Anthony, Oliver; D/IPR Formalities Section, Poplar 2, MOD (PE) Abbey Wood #19, Bristol BS34 8JH (GB).		(81) Designated States: CN, GB, JP, KR, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i>

(54) Title: A LIGHT EMITTING DEVICE AND TRANSISTOR**(57) Abstract**

A light emitting device for generating at least one beam of output radiation from an input current of electrons comprises at least two p-n junction devices for converting the input current of electrons into photons, wherein the p-n junction devices are electrically connected in series such that the input impedance of the light emitting device is substantially equal to the sum of the individual impedance of the p-n junction devices. Hence the quantum efficiency of the light emitting device is substantially equal to the sum of the individual quantum efficiencies of the p-n junction devices. In a preferred embodiment, the light emitting device comprises a plurality of p-n junction devices connected in series such that the input impedance of the light emitting device is equal to 50 Ω without the need for additional circuitry or impedance matching elements. The device may therefore have a 50 Ω impedance over a broad frequency band, limited by the modulation frequency limit of the individual p-n junctions. Typically, the p-n junctions may be AlGaAs, AlGaInP, AlGaInAs or AlGaInAsP laser diode devices. The invention also relates to an optically coupled bipolar transistor device.

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

A LIGHT EMITTING DEVICE AND TRANSISTOR

The invention relates to a laser device capable of operating with enhanced quantum efficiency across a broadband of modulation frequencies. In particular, this may be achieved in the fixed impedance ($50\ \Omega$) microwave circuitry without the need for complex impedance matching. The invention also relates to a transistor-like device capable of delivering current gain across a broadband of frequencies, again without the need for complex impedance matching. The invention has advantages in the field of fibre optic communications and transmissions, and in RF applications due to its broadband nature and the ability to produce current gain.

The operation of a conventional common base bipolar transistor is well known in the prior art, for example see *W. Shockley, Bell Syst. Technology, J. 28, 435, 1949*. The crucial feature of this transistor is the transit of current from the low impedance input (base/emitter) circuit to a high impedance output (base/collector) circuit by means of minority carrier diffusion across a thin (base) semiconductor layer. The reverse biased collector output can therefore deliver current into a relatively high impedance load resistor.

It has been proposed to use a Beam Of Light Transistor (BOLT) [*R. Rediker et al., Proc. IEEE 51, 218 1963*] in which the transfer of current is achieved by converting the minority carriers into photons before transit across a "base" layer in the device and then converting the photons back to a current at the output. However, the optical transfer cannot be achieved efficiently in such devices and current losses are always encountered and therefore current gain cannot be achieved.

In conventional microwave technology, to increase microwave power at high frequencies several individual transistors may be connected in parallel in order to achieve current gain. Such small devices have a high speed of operation, but the resistance of the parallel arrangement is small. In microwave circuitry, where the majority of microwave generation, transmission, reception and cable hardware is of 50 Ω impedance, it is difficult to match the low impedance of these devices to the 50 Ω hardware over a broad frequency range. As conventional electronic transistors are three terminal devices, they may only be combined in parallel. This restricts the possibilities for thermal management.

Another problem associated with electrical transistors is that the displacement currents produced limit the speed of operation of the transistor. Furthermore, as the input circuit and the output circuit are coupled problems can arise due to feedback from the output circuit to the input circuit.

The transistor of the present invention overcomes the problems in known electronic and optical transistor devices. Furthermore, its characteristics and design make it suitable for application to fibre optic communications and the optical distribution of radiofrequency, microwave, mm-wave and digital signals in electronic systems such as phased array radars. In the field of fibre optic links, conventional semiconductor lasers are commonly used. The current technology for achieving directly modulated, broadband, high speed fibre optic links has been developed principally for digital communications systems. Typically, such fibre optic links operate at an upper frequency limit of around 10 GHz set by the response capability of the laser to the input signal. Conventional broadband fibre optic links, however, cannot deliver signal gain unless either electronic or optical amplifiers are included in the signal path. Furthermore, for use in fixed impedance environments, such as microwave circuits, a relatively narrow band impedance transformation is needed to match the low impedance laser in order to minimise signal loss.

The invention overcomes the narrow bandwidth limitation associated with conventional lasers in that no impedance transformation is necessary. The laser may also be used in a transistor device to provide current gain across a broadband of modulation frequencies, from DC up to typically 30 GHz. For the purpose of this description, references to frequency shall be taken to mean modulation frequency rather than the optical emission frequency of the laser. Such a device therefore overcomes the impedance transformation problems encountered when conventional transistors are used in 50 Ω microwave circuitry. The laser is also capable of providing both AC and DC signal gain in combination with a photodiode, which conventional lasers cannot deliver.

According to one aspect of the invention, a light emitting device, having an input impedance and a device quantum efficiency, for generating at least one beam of output radiation from an input current of electrons comprises;

at least two light emitting means, each having an impedance and an individual quantum efficiency, for converting the input current of electrons into a beam of output radiation,

characterised in that the light emitting means are electrically connected such that the device quantum efficiency is greater than or equal to the individual quantum efficiency of one of the light emitting means.

In one embodiment of the invention, the light emitting means are electrically connected in series such that the input impedance of the light emitting device is substantially equal to the sum of the impedances of the light emitting means and the device quantum efficiency may be substantially equal to the sum of the quantum efficiencies of the light emitting means.

In another embodiment of the invention, the light emitting means may be electrically connected in any one of a parallel connection or in a series parallel connection.

In a preferred embodiment, the light emitting means may be electrically connected such that the input impedance of the light emitting device is substantially equal to 50 Ω without additional circuitry or impedance matching elements.

The light emitting means, having a modulation frequency limit, wherein the input impedance of the light emitting device is substantially equal to 50 Ω across a frequency range substantially from DC to the modulation frequency limit of each of the light emitting means.

The light emitting means may be p-n junctions, for example laser diodes or light emitting diodes. Typically, the laser diode devices may be any one of AlGaAs, AlGaInP, AlGaInAs or AlGaInAsP laser diode devices. The p-n junctions may have an end face coated with a reflective coating.

According to another aspect of the invention, an optically coupled transistor for generating an output electrical signal comprises;

the light emitting device as herein described for emitting at least two beams of output radiation and

at least one photodetector for detecting the beams of radiation output from the light emitting device and for converting the beams of output radiation into an output electrical current.

In one embodiment of the optically coupled transistor, the one or more photodetectors may be a photodiode device

For example, the optically coupled transistor may comprise at least two photodetectors, wherein the photodetectors are connected in any one of a series connection, a parallel connection or a series parallel connection.

The optically coupled transistor may also comprise one or more optical fibres for transmitting the beams of output radiation to the one or more photodetectors.

According to another aspect of the invention, a fibre optic link comprises one or more optical fibres having an input endface and an output endface, and also comprising the light emitting device as herein described, wherein the light emitting device is situated at the input endface of one or more optical fibres such that the beams of radiation output from the light emitting device are input to the one or more optical fibres.

According to another aspect of the invention, a method for distributing an input signal into a plurality of output channels comprises the steps of;

(i) outputting two or more beams of radiation from the light emitting device as herein described comprising at least two light emitting means and

(ii) inputting each of the two or more beams of output radiation into one of the output channels,

whereby the light emitting means are connected such that the device quantum efficiency is greater than or equal to the individual quantum efficiency of one of the light emitting means.

In a preferred embodiment, the method may comprise the step of inputting each of the two or more beams of output radiation into a different one of the output channels.

According to another aspect of the invention, an optical repeater for receiving an optical input signal and generating one or more optical output signals comprises;

a photodetector for receiving the optical input signal and converting the optical input signal into an electrical signal and

the laser device of any of claims for receiving the said electrical signal and outputting one or more optical signals.

In a preferred embodiment, the optical repeater also comprises amplification means for amplifying the electrical signal output from the photodetector.

The invention will now be described by example only with reference to the following figures in which;

Figure 1 shows the epitaxial layer structure of the laser device of the present invention,

Figure 2 shows the typical dimensions of the device shown in Figure 1

Figure 3 shows a three-dimensional illustration of the laser device shown in Figure 1,

Figure 4 shows the electrical circuit corresponding to a AlGaAs laser diode which may be used in the invention,

Figure 5 shows a diagram of a conventional common base electronic transistor,

Figures 6(a) and 6(b) show schematic diagrams of an optically coupled transistor (OCT) comprising the laser device shown in Figure 1,

Figure 7 shows the current transfer results of an optically coupled transistor comprising a ten-element laser device, and

Figure 8 shows the frequency response of the optically coupled transistor comprising a ten-element laser device and

Figure 9 shows a diagram of an optical repeater comprising the light emitting device shown in Figure 1.

Referring to Figure 1, in one example of the invention a laser device 1 may comprise a number of discrete p-i-n laser diode elements 2a,2b in an array. Only two laser diodes are shown in the figure but in practice a greater number of laser diodes may be included in the device. For example, each individual laser diode 2a,2b may be a AlGaAs p-i-n laser diode comprising a top layer 3 of highly doped, low resistance p-type GaAs, on a moderately doped upper cladding layer 4. This is formed on a waveguide layer 5 which is undoped, formed on a lower cladding layer 6, wherein the waveguide layer 5 has a lower band gap than either the upper or lower cladding layers 4,6. Embedded within the waveguide layer 5 are two 10 nm GaAs quantum wells (not shown) that provide optical gain. The combination of the upper and lower cladding layers 4,6 forms a double heterojunction barrier layer confining both photons and carriers to the undoped waveguide layer 5.

Typically, layer 3 may be p-type GaAs (typical thickness 0.1 μm , typical doping concentration $5 \times 10^{19} \text{ cm}^{-3}$), the upper cladding layer 4 may be AlGaAs (40%), (typical thickness 1.5 μm , typical doping concentration $5 \times 10^{17} \text{ cm}^{-3}$) and the lower cladding layer 6 may be n-type AlGaAs (40%) (typical thickness 1.5 μm , typical doping concentration $5 \times 10^{17} \text{ cm}^{-3}$). The waveguide layer 5 may typically be undoped AlGaAs (20%) (typical thickness 0.23 μm).

The cladding layer 6 of each laser element 2a,2b is situated on a highly doped n-type layer 7 which forms a low resistance GaAs contact for the laser diode 2a,2b. Typically, this cladding layer 7 may be a GaAs layer having a thickness of between 0.5 and 1.0 μm and a doping concentration of $2 \times 10^{18} \text{ cm}^{-3}$. The percentage indications refer to the percentage of Al in the layers. Each laser diode 2a,2b has an insulating layer, for example polyimide, covering the mesa and a plated electrical layer 9 covering that. The p-type layer 3 is in contact with the plated electrical layer 9 at junction 10 and the n-type layer 7 is in contact with the plated electrical layer 9 at junction 11. The laser array 1 may be constructed using integrated photonics technology techniques which would be conventional to one familiar with the art.

For each laser element 2a,2b to operate without undue RF noise, optical waveguide operation should be single mode. The typical dimensions of the laser having single mode optical waveguide operation are shown in Figure 2. In this example, the so-called "ridge" waveguide (i.e. layer 3 and upper region of 4) has a width of $3.5\text{ }\mu\text{m}$ and will support only one mode when lasing. The ridge is supported on a mesa (layers 5,6 and lower region of 4), typically having a width of $25\text{ }\mu\text{m}$. This ridge must be wide enough for the optical mode supported not to be influenced by the edges of the mesa which would otherwise support higher modes also. Typically, layer 7 extends beyond the width of the mesa, on one side only, by around $50\text{ }\mu\text{m}$. Although single mode lasers are necessary for high frequency operation, due to mode noise, for lower frequencies multi mode lasers or light emitting diodes may be used in the device 1.

As the highly doped n-type layer 7 which forms a low resistance GaAs contact for each individual laser is completely removed on one side of each laser element 2a,2b, this leaves each laser electrically isolated from its neighbour by the semi-insulating GaAs substrate layer 7. The laser diodes 2a,2b are connected in series by means of the metal layer 9.

Returning to Figure 1, current, I_e , injected through the top layer 3, on the first laser 2a in the series, passes down through the p-i-n junction (layers 4,5 and 6 respectively) emitting light from the quantum wells within the intrinsic region 5. The current then leaves the first laser 2a through the bottom layer 7, and passes over the mesa of the next element in the series by means of the plated electrical layer 9. The current is injected into this next laser element 2b through the top layer 3 and passes down through this p-i-n junction, as before. Figure 3 shows a three-dimensional view of a section of an array of discrete lasers (two lasers 2a,2b are shown in the figure), where the laser elements 2a,2b are connected in series. The figure also shows the junction 10, a p-type contact, and junction 11, an n-type contact. Each laser shares a contact layer 9 with its neighbour on either side except for the first and last lasers in the array whose unshared contacts act as the input and output contacts for the whole array.

The laser device 1 shown in Figure 1 is an illustration of an integrated version of the device which would be capable of operating at high speeds. In fact, the elements may be any light emitting p-n junction device. Light Emitting Diodes (LEDs) are also quite suitable. For the purpose of this specification, the term p-n junction should be taken to include any variations of the semiconductor device structure having p-type and n-type components, including p-i-n junctions. Examples of lasers which may be used include AlGaAs, AlGaInAs, AlGaInP and AlGaInAsP devices.

The laser device 1 shown in Figure 1 is an integrated device which may be constructed on a single chip. An important feature of the integrated device 1 shown in Figure 1 is that each laser diode 2a,2b is on an insulating substrate with the only electrical connection between them being the plated metal layer 9 connecting the laser diodes 2a,2b in series. However, the invention is not intended to be limited to integrated devices and may also take the form of a series of discrete components connected in series in a circuit. An integrated device, however, does have the advantage of a higher speed of operation than a circuit manifestation.

One key functional advantage of the laser device 1 is that the external incremental quantum efficiency of the array is greater than that of a single laser. The external incremental quantum efficiency (η_{LD}) of a laser is conventionally measured in photons per electron and is a key measure of laser efficiency. For the purpose of this description, the term "quantum efficiency" shall be taken to mean the external incremental quantum efficiency. The net quantum efficiency of the laser device 1 is the sum of all the individual efficiencies of each element. This allows the device to operate with a quantum efficiency of greater than 100%. This has important advantages for the transistor action and in fibre optic signal distribution where this efficiency determines the overall current or signal transfer efficiency.

A further advantage of the laser device 1 is that the impedance may easily be matched to the 50 Ω fixed impedance of microwave circuitry, without the need for additional circuitry or matching elements. However, a biasing network is needed so that the light emitting device 1 may be biased above laser threshold current. The details of a suitable biasing network would be conventional to one familiar with the art and are not described in this specification. The impedance characteristic may be understood by considering the input impedance of a single laser diode. For example, Figure 4 shows an equivalent circuit for a forward biased, one-sided, abrupt p-n junction. The circuit shown is approximately equivalent to a typical AlGaAs laser diode device where R_s represents the parasitic series resistance, r_e represents the dynamic junction resistance, C_j represents the depletion capacitance and C_d represents the diffusion capacitance. The operation of a p-n junction is conventional to one skilled in the art and the terms referred to above and the origin of the quantities defined as such would be understood by such a person.

In a AlGaAs laser diode device, having the equivalent circuit shown in Figure 4, and at a current density of about $1 \times 10^4 \text{ A cm}^{-2}$ into a junction area of about 10^{-5} cm^2 , the value of the quantity, R_s , is typically 5 Ω . The dynamic junction resistance, r_e , is given by;

$$\frac{kT}{qI} = \frac{25mV}{I}$$

where I is the current and q is the electronic charge. A current of 100 mA therefore gives a dynamic junction resistance, r_e , of 0.25 Ω . The depletion capacitance, C_j (at a forward bias of $1 \times 10^4 \text{ A cm}^{-2}$) is typically around 5 pF. The diffusion capacitance, C_d , has an impedance in the region of $5 \times 10^{-3} \Omega$ at a frequency of 10 GHz.

The circuit shown in Figure 4 therefore behaves as if it were a simple 5 Ω resistor at all frequencies below approximately 20 GHz. This resistance, R_s , is parasitic and is at least 10 times the value of the dynamic junction resistance, r_e . Hence if ten laser elements are included in the laser device 1 shown in Figure 1, this gives an input impedance of 50 Ω . Furthermore, the device also has a quantum efficiency ten times greater than that of a single device and if the parasitic resistance, R_s , can be reduced further (i.e. to less than around 5 Ω) it is possible to achieve still larger quantum efficiencies by including more emitters, whilst retaining the 50 Ω impedance.

In alternative simple series connections to achieve 50 Ω impedance, the lasers can be combined in combinations of series and parallel. For example, if the lasers have a series resistance of 10 Ω , a simple series combination would consist of 5 lasers. As an alternative, two lasers could be connected in parallel, giving an impedance of 5 Ω , with each parallel combination then being connected in series to give a final impedance of 50 Ω .

According to another aspect of the invention, the device may also be combined with a photodetector, such as a conventional semiconductor p-i-n photodiode under reverse bias, to produce a transistor-like device which operates on the basis of the optical transfer of photons rather than the transfer of minority carriers as in conventional transistor devices.

Referring to Figure 5, a conventional common base electronic transistor may typically comprise an n-p-n junction, comprising an emitter 12 (n-type region), a base 13 (p-type region) and a collector 14 (n-type region). The input emitter 1 is forward biased by means of an applied voltage 15 and the output of the collector 3 is reverse biased by means of an applied voltage 16. Electrons transit the p-type base layer 13 where a small fraction of the electrons are lost by recombination and produce a small base current I_b . The base current is given by $I_b = I_e - I_c$, where I_e and I_c are the currents flowing in the base/emitter input circuit and the base/collector output circuit respectively, as indicated in the figure.

The base/collector "output" circuit also comprises a load resistance 17 (R_L) which may have a relatively high impedance. Since the base/emitter "input" circuit is of low (DC and AC) impedance and the output of the reverse biased collector can deliver current into the relatively higher impedance load impedance, R_L , the transistor is therefore capable of delivering power gain.

However, for power gain to be delivered by the transistor in 50 Ω impedance circuits, such as for microwave generation, transmission, reception and cable hardware requires current gain.

Furthermore, the bipolar transistor shown in Figure 1, is not capable of delivering any current gain since the collector current is always less than the emitter current because of the small base current.

Referring to Figure 6(a), a second aspect of the invention relates to a transistor 18 capable of delivering current gain, including the laser device shown in Figure 1. The transistor also includes a photodetector or an array of photodetectors connected in a series, parallel or series parallel arrangement. The transistor shall hereinafter be referred to as an optically couple transistor device, or OCT device. The OCT device may be constructed using integrated-on-chip monolithic photonics technology.

The OCT device 18 comprises an input circuit 19 (the "base/emitter" circuit) and an output circuit 20 (the "base/collector" circuit) which may have a common "base" connection 30. The input circuit 19 comprises the laser device 1 shown in Figure 1. The laser device 1 in the figure is shown to comprise three p-n junctions 21. As discussed previously, the laser device 1 may comprise any number of semiconductor devices having a p-n type structure (as defined earlier) and may be devices integrated on a chip or connected in a circuit. For example, the junctions 21 may be lasers or light emitting diodes (LEDs) capable of efficiently converting an electrical current into photons. Furthermore, although three junctions 21 are shown, in practice the device may comprises a greater number of junctions 21 connected in series, in parallel or in series parallel, giving a total impedance of $50\ \Omega$.

The input circuit 19 also comprises a voltage source 22 for forward biasing the p-n junctions 21. The output circuit 20 comprises a p-n junction 23, reverse biased by means of a voltage source 24, which therefore acts as a photodiode for converting incident radiation into an electrical current. For example, the collector 23 may be a silicon photodiode detector. The output circuit 20 has an output load resistance 25 (R_L) which may be relatively large (e.g. $50\ \Omega$).

An enlarged view of a series of three p-n junctions 21 is shown in Figure 6(b). In this embodiment, each junction 2 comprises an n-type region 26 and a p-type region 17, wherein the p-type region 27 of one junction is in connection with the n-type region 26 of the adjacent p-n junction such that the junctions are connected in series, in parallel or in series parallel.

The emitter current, I_e , i.e. the current flowing in the input circuit 19, passes through the p-n junctions 21 each of which converts the input electrical current into photons 29. These photons 29 are transmitted to the output circuit 20 and are collected by the photodiode 23 (or photodiodes) which converts the photons back to an equivalent electrical current, I_c . Although only one photodiode detector 23 is shown in this example, in practice this may be a number of photodiode detectors connected in series, in parallel or in series parallel. For example, it may be preferable have one photodiode detector for each of the p-n junctions 21 of the laser device 1. An OCT comprising a plurality of photodiode detectors may be capable of operating at higher speeds than an OCT comprising a single, large photodiode device.

If the optical conversion and transfer functions OCT are efficient, the device is capable of delivering current gain. The incremental current transfer efficiency, η_{CT} , for a single p-n junction may be defined by;

$$\eta_{CT} = \eta_{LD} \cdot \eta_{OP} \cdot \eta_{PD}$$

where η_{LD} is the external incremental quantum efficiency (in photons per electron) of the light emitting device 21, η_{OP} is the optical transfer efficiency between the light emitting device 21 and the reverse biased p-i-n photodiode 23 and η_{PD} is the conversion efficiency (in electrons per photon) of the photodiode 23. For example, if each p-n junction 21 has a quantum efficiency (η_{LD}) of, say, 50%, and the optical transfer efficiency and photodiode efficiency are both 100%, then the three-junction OCT device 18 generates an incremental output current, ΔI_C , from the photodiode 23 of $3 \eta_{CT} \Delta I_e$, that is $\Delta I_C = 1.5 \cdot \Delta I_e$, where I_e is the current in the laser. Hence the current output from the OCT device is greater than the input current. For 'n' p-n junctions in the array the net current transfer efficiency is

$$\eta_{CT} = n \cdot (\eta_{LD} \cdot \eta_{OP} \cdot \eta_{PD})$$

The current transfer results obtained for a OCT device, comprising an array of ten laser elements in series, are shown in Figure 7 which shows the relationship between (i) AC photodiode current gain and (ii) DC photodiode current as a function of the laser bias current. To obtain these results, the ten element laser array was fed from a $50\ \Omega$ source and the photodiode had a $50\ \Omega$ load resistance, R_L .

The threshold current of the array was similar to that of the individual laser elements ($\sim 21\text{ mA}$). Referring to Figure 7 for an input current increment ΔI_e of, say, 14 mA above threshold, the incremental output photodiode current ΔI_c is around 28 mA , giving an (incremental) current gain of a factor of two. The incremental gain is a key parameter of the DC and RF performance of the OCT device and also fibre optic links which incorporate the device.

The frequency response of the OCT device comprising a ten-element laser array in electrical series is shown in Figure 8 which shows (i) the AC photodiode current gain and (ii) the AC laser current gain over a frequency range of 25 kHz to 5 MHz . The gain is seen to reduce at approximately 3 MHz . This is consistent with the response speed limit of the silicon photodiode used in the OCT device. This gain reduction is, however, an artefact of the measurement and the response speed limit of the OCT may be increased by using a faster photodiode device.

The primary advantage of the OCT device is its ability to deliver current gain (as well as power gain). Furthermore, this may be achieved across a broad frequency band in a fixed impedance circuit. The impedance matching is achieved from DC to the operating limit of the laser.

Another advantage of the device is that the transit between the input circuit 8 and the output circuit 9 is optical rather than electrical and hence there are no displacement currents produced. Furthermore, as the input circuit 19 and the output circuit 20 are decoupled, this avoids any feedback from the output circuit 20 to the input circuit 19 which is a problem in conventional electronic transistors.

A further advantage of decoupling the input and output circuits 19,20 is that the transistor is then two physically separated and electrically independent two-terminal circuits, unlike the conventional transistor (Figure 1) which is a three terminal device. Each of the two-terminal circuits therefore has more design degrees of freedom. The circuits 19,20 may therefore be more easily be distributed to alter the impedance of the input circuit 19 and the output circuit 20 using series or parallel circuit configurations with current combining and splitting being achieved in the optical domain, hence having no electrical penalty. The ability to distribute the transistor as two two-terminal circuits 19,20 also allows improved power outputs and thermal management.

In an uncoated device the optical coupling efficiency (η_{op}) is less than 50% since only light from one end of the laser device 1 is collected by the photodiode in figure 6(a). In another embodiment of the OCT device 18 shown in Figure 6(a), a highly reflective coating may be applied to one end of each p-n junction element 21, thus forcing all the output radiation to be emitted from the other end of the element. This maximises the light received at the photodiode. Alternatively, two photodiodes may be included in the OCT, with one photodiode situated on each side of the laser device 1. The currents output from the photodiodes may then be combined in parallel to give twice the current achieved in a single-photodiode device.

In another embodiment of the OCT device 18 the p-n junctions could be series connected vertical cavity surface emitting lasers grown on top of collector photodiodes in parallel connection with their optical outputs being transmitted through a transparent "base" contact layer.

In order to achieve a greater current gain than is possible with a single OCT, a number of OCTs may be connected in series, in parallel or in series parallel (i.e. cascaded).

As the input and output circuits 19,20 are decoupled, the OCT device has useful application in the field of fibre optic links. For example, the optical transit between the input circuit 19 and the output circuit 20 may be achieved over a large distance, up to say 100 km. Thus, simple optical fibre links with broadband characteristics having reduced insertion loss, and possibly gain, may be realised by deploying the input and output circuits 19,20 of the transistor at either end of the optical fibre link.

This may be of particular advantage in the field of fibre optic links where signal insertion losses into fibre may be reduced. Furthermore, the individual elements in the laser device may be coherently locked such that coherent summation into a single passive waveguide output is obtained for input to a single optical fibre. This is more convenient than having to use a ribbon of optical fibres.

The laser device may also be used in the division of an RF signal through many fibre optic channels. Conventionally, this may be done by splitting the optical signal between each channel and then amplifying each of the split signals (optical or RF amplification) to recover the original signal power. Alternatively, this may be done by splitting the amplified RF signal and transmitting each split signal to a separate channel each having its own light source. However, with the laser device 1 each laser element can provide sufficient power, without the need for amplification, to a separate optical fibre connected to each element in the array. This avoids any penalty in the signal division.

The OCT has so far been described as a device for converting an input electrical signal into an optical signal and then outputting an output electrical signal. An alternative configuration for the OCT is as an optical repeater, in which an input optical signal is converted into an electrical signal which is then converted back into one or more optical output signals. One possible configuration of the optical repeater is shown in Figure 9. In practice, it may be preferable to include an amplifier and biasing network in the laser input circuitry to allow for the inefficiencies that arise in the optical conversion processes. The details of a suitable biasing network would be conventional to one familiar with the art.

Claims

1. A light emitting device, having an input impedance and a device quantum efficiency, for generating at least one beam of output radiation from an input current of electrons comprising;

at least two light emitting means, each having an impedance and an individual quantum efficiency, for converting the input current of electrons into a beam of output radiation,

characterised in that the light emitting means are electrically connected such that the device quantum efficiency is greater than or equal to the individual quantum efficiency of one of the light emitting means.

2. The light emitting device of claim 1 wherein the light emitting means are electrically connected in series such that the input impedance of the light emitting device is substantially equal to the sum of the impedances of the light emitting means and wherein the quantum efficiency of the device is substantially equal to the sum of the quantum efficiencies of the light emitting means.

3. The light emitting device of claim 1 wherein the light emitting means are electrically connected in any one of a parallel connection or in a series parallel connection.

4. The light emitting device of any of claims 1-3 wherein the light emitting means are electrically connected such that the input impedance of the light emitting device is substantially equal to 50 Ω without additional circuitry or impedance matching elements.

5. The light emitting device of claim 4, wherein each of the light emitting means have a modulation frequency limit and wherein the input impedance of the light emitting device is substantially equal to 50 Ω across a frequency range substantially from DC to the modulation frequency limit of each of the light emitting means.

6. The light emitting device of claim 1 wherein the light emitting means are p-n junctions.

7. The light emitting device of claim 6, wherein the p-n junctions are laser diodes or light emitting diodes.
8. The light emitting device of claim 7, wherein the laser diode devices may be any one of AlGaAs, AlGaInP, AlGaInAs or AlGaInAsP laser diode devices.
9. The light emitting device of claim 7, wherein the p-n junctions each have an end face coated with a reflective coating.
10. An optically coupled transistor for generating an output electrical signal comprising;

the light emitting device of any of claims 1-5 for emitting at least two beams of output radiation and

at least one photodetector for detecting the beams of radiation output from the light emitting device and for converting the beams of output radiation into an output electrical current.
11. The optically coupled transistor of claim 10 wherein the one or more photodetector is a photodiode device.
12. The optically coupled transistor of claim 10, comprising at least two photodetectors, wherein the photodetectors are connected in any one of a series connection, a parallel connection or a series parallel connection.
13. The optically coupled transistor of claim 10, comprising one or more optical fibres for transmitting the beams of output radiation to the one or more photodetectors.

14. A fibre optic link comprising one or more optical fibres having an input endface and an output endface, and also comprising the light emitting device of claim 4,

wherein the light emitting device is situated at the input endface of one or more optical fibres such that the beams of radiation output from the light emitting device are input to the one or more optical fibres.

15. A method for distributing an input signal into a plurality of output channels comprising the steps of;

(i) outputting two or more beams of radiation from the light emitting device of claim 1 comprising at least two light emitting means and

(ii) inputting each of the two or more beams of output radiation into one of the output channels,

whereby the light emitting means are connected such that the device quantum efficiency is greater than or equal to the individual quantum efficiency of one of the light emitting means.

16. The method of claim 15 comprising the step of inputting each of the two or more beams of output radiation into a different one of the output channels.

17. An optical repeater for receiving an optical input signal and generating one or more optical output signals comprising;

a photodetector for receiving the optical input signal and converting the optical input signal into an electrical signal and

the laser device of any of claims 1-5 for receiving the said electrical signal and outputting one or more optical signals.

18. The optical repeater of claim 17 and also comprising amplification means for amplifying the electrical signal output from the photodetector.

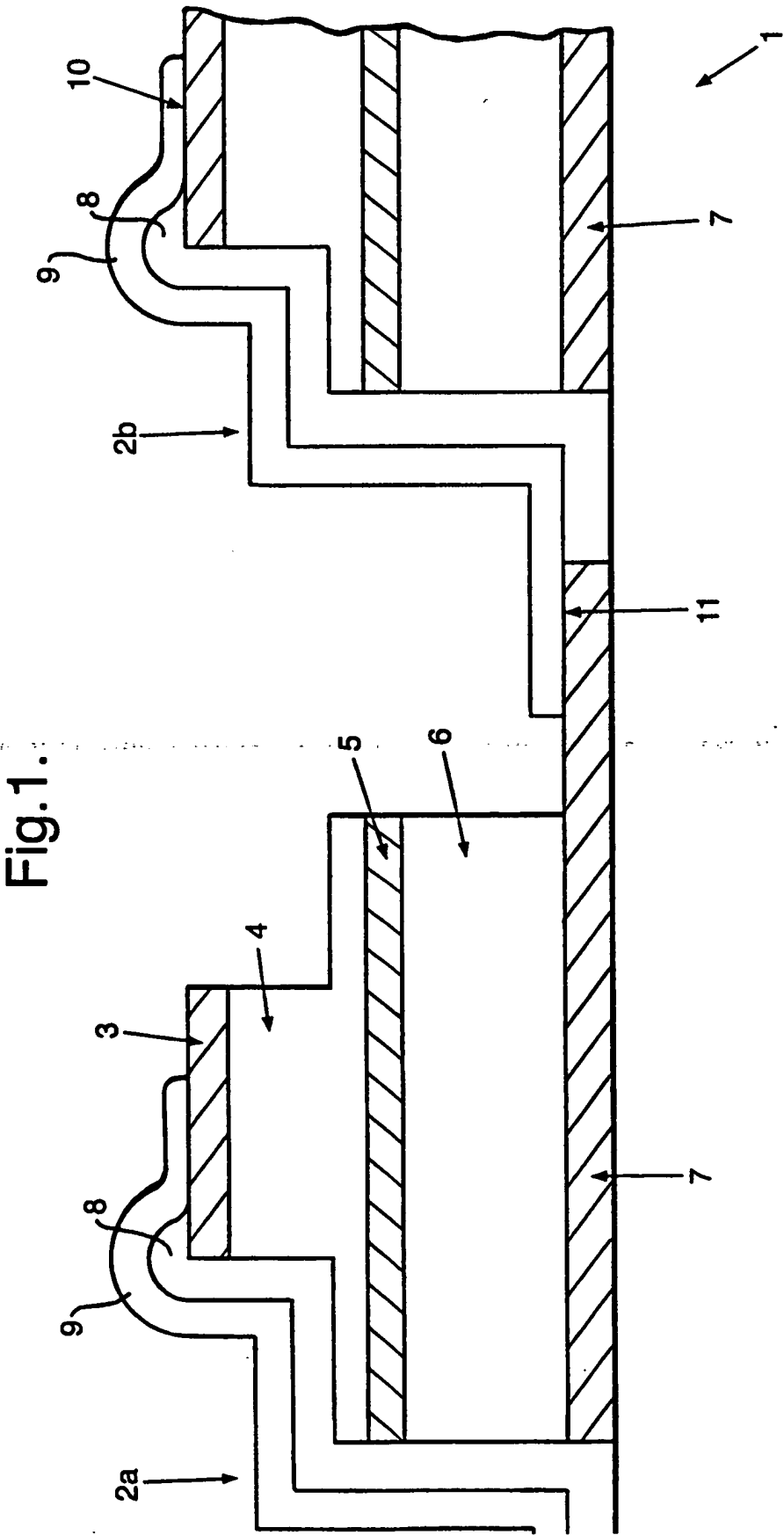


Fig.1.

2/6

Fig.2.

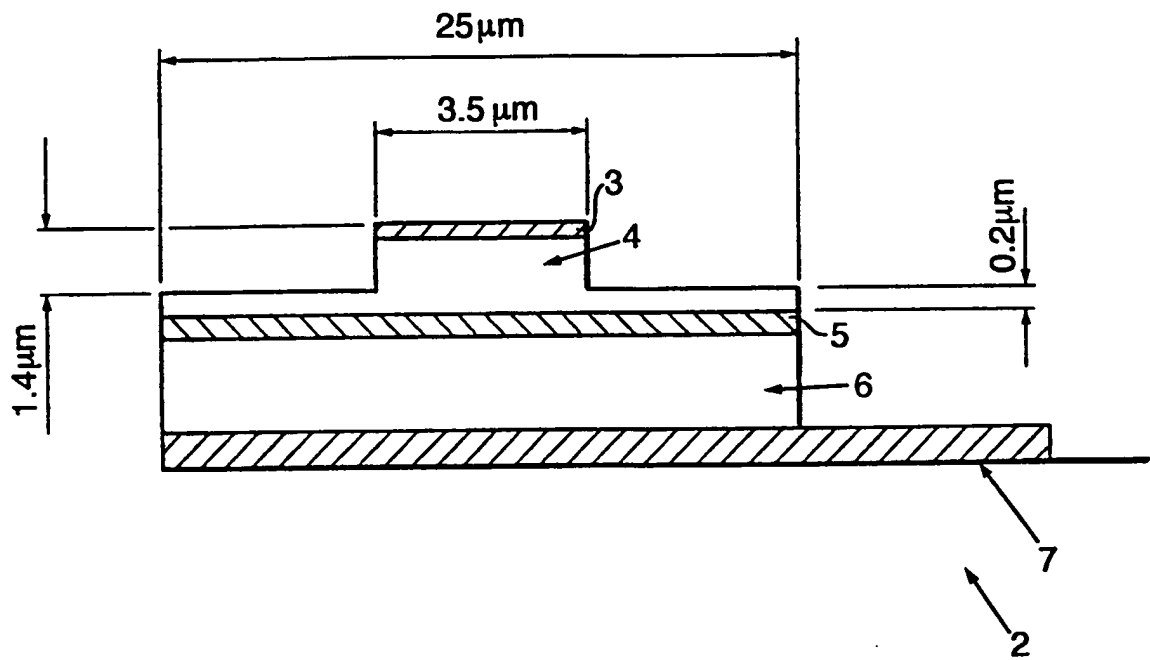
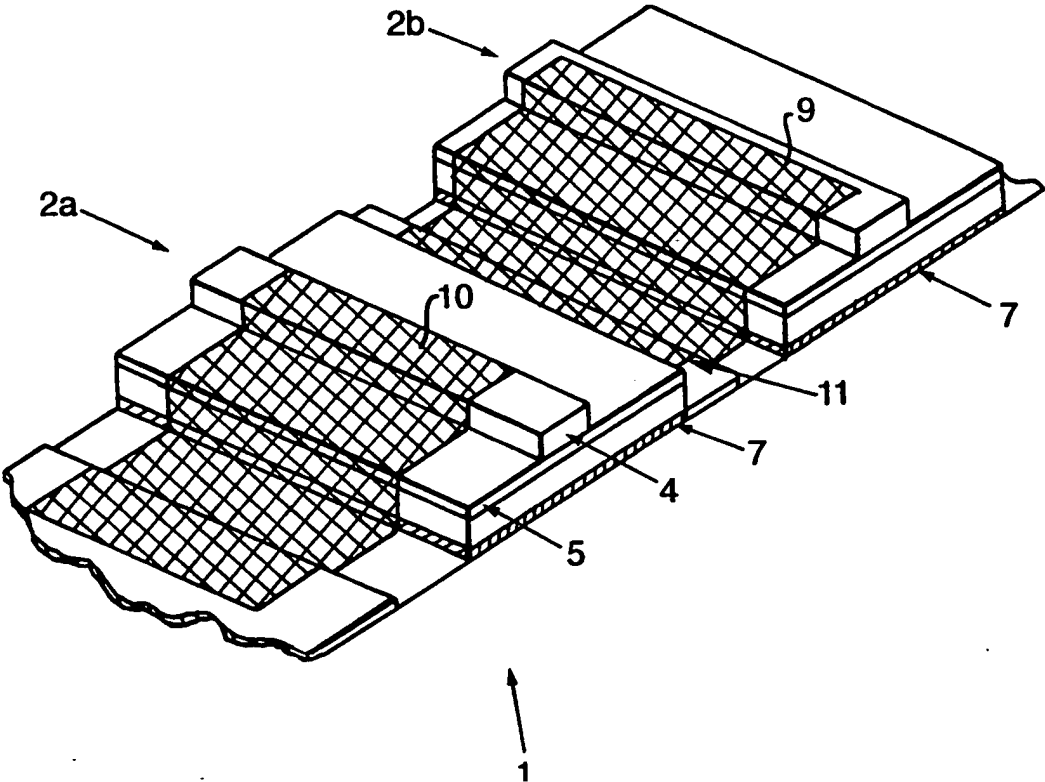


Fig.3.



3/6

Fig.4.

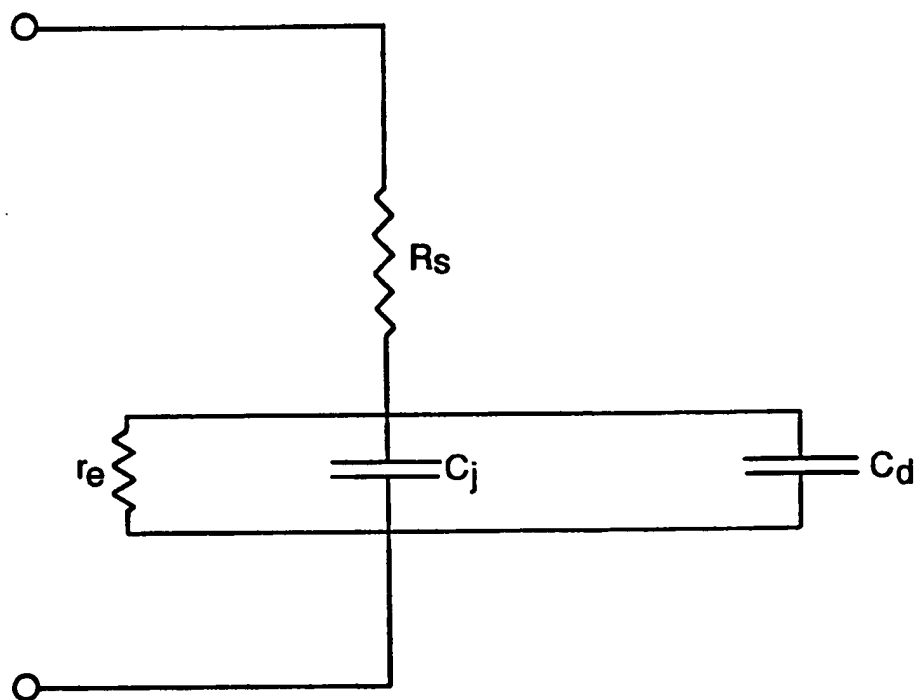
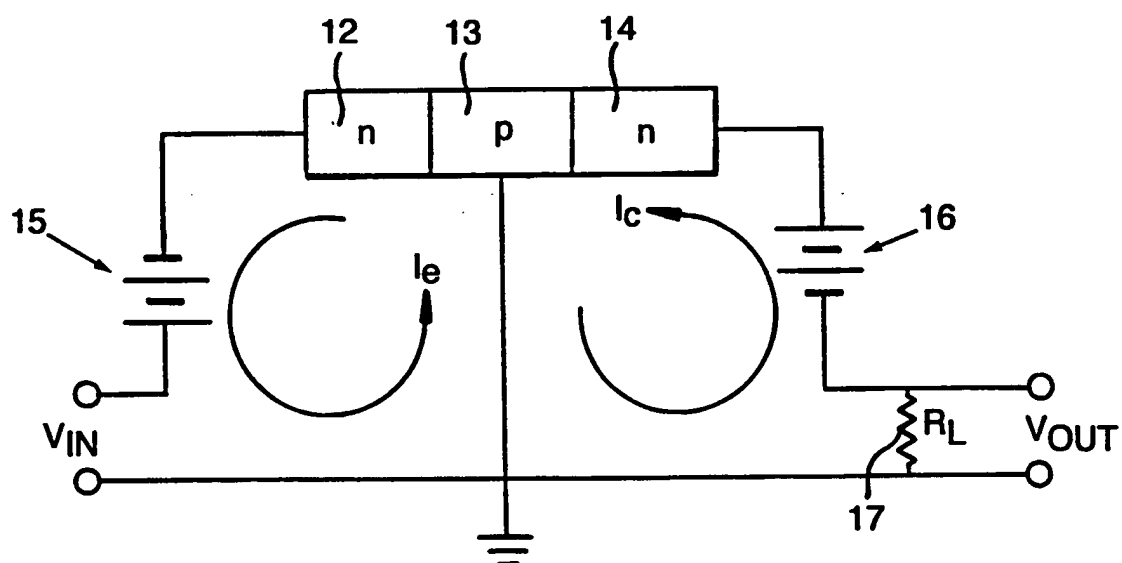


Fig.5.

PRIOR ART



4/6

Fig.6(a).

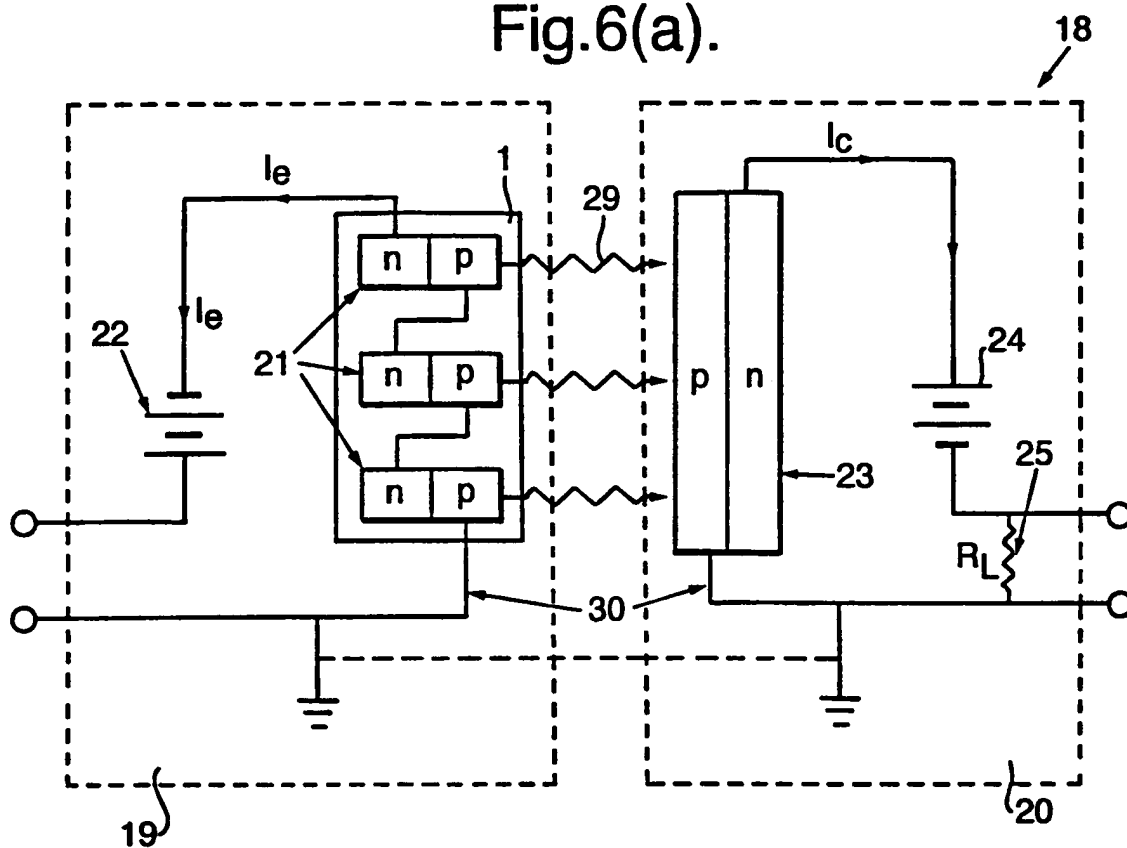
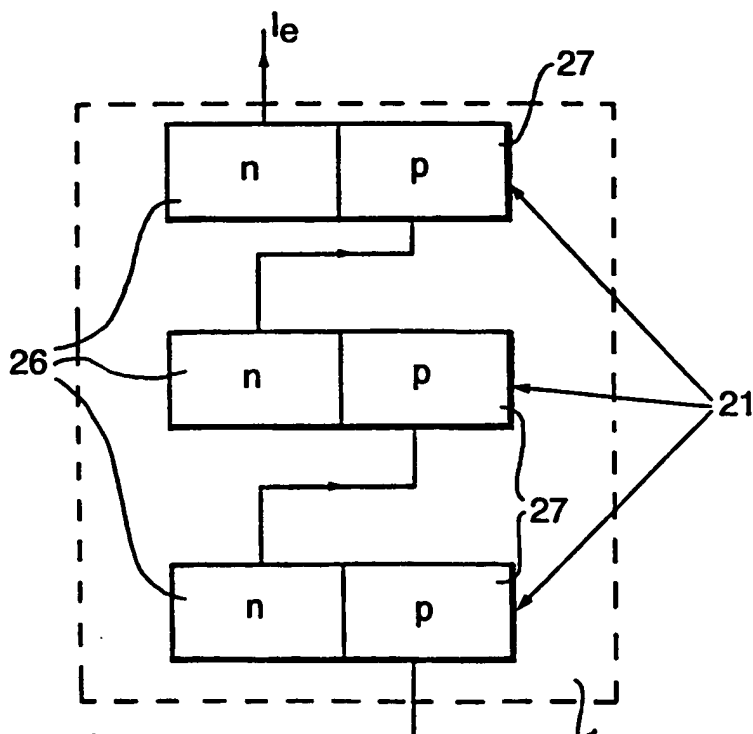


Fig.6(b).



5/6

Fig.7.

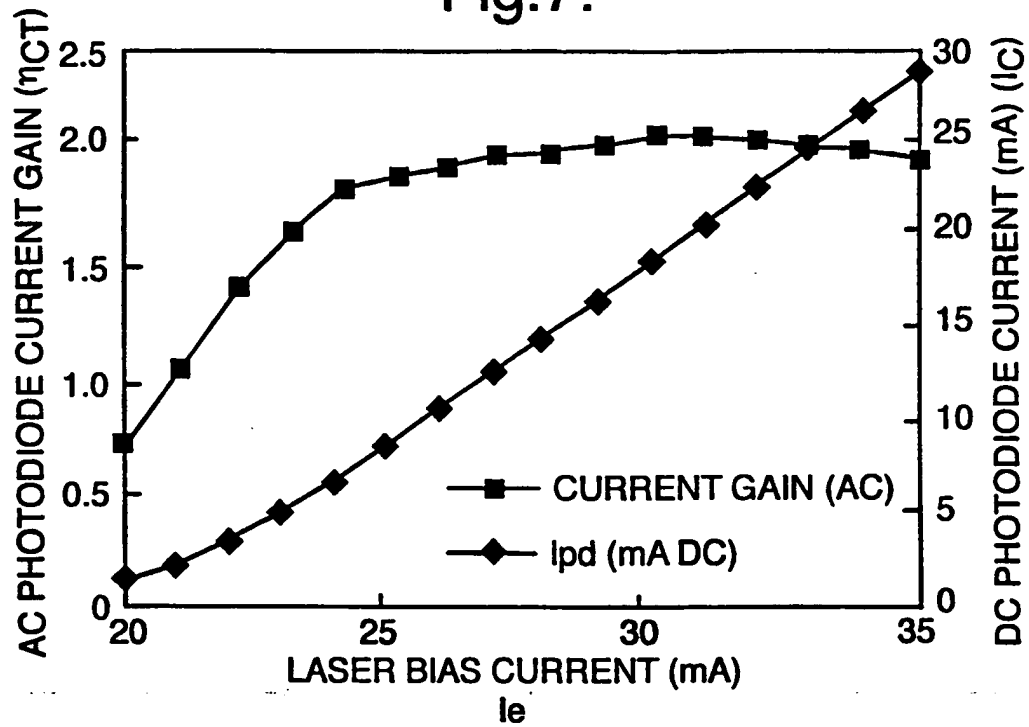


Fig.8.

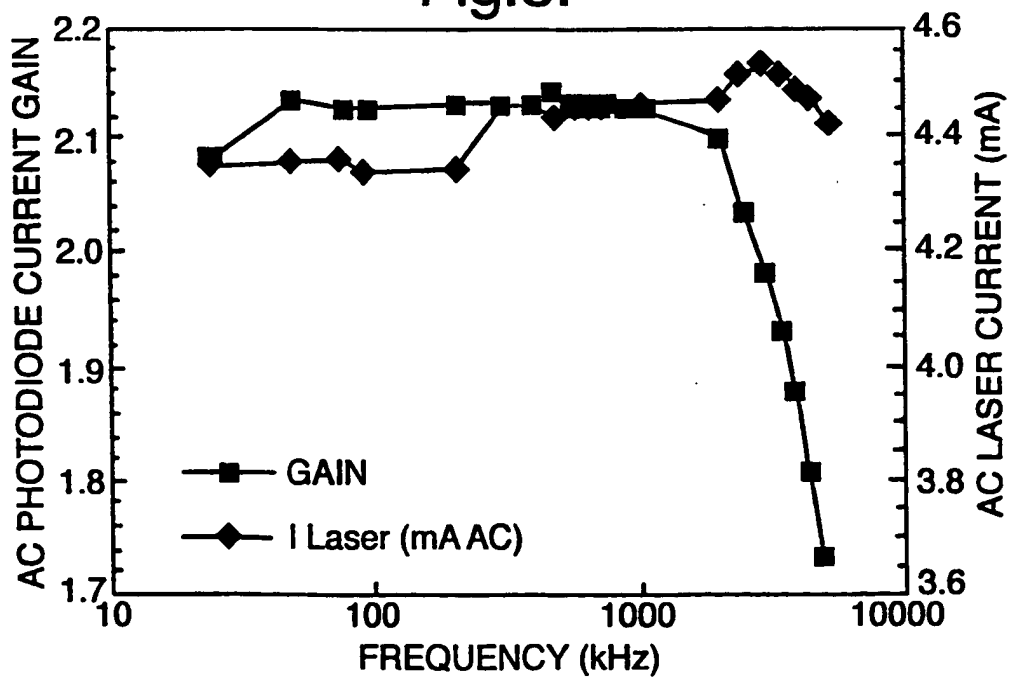
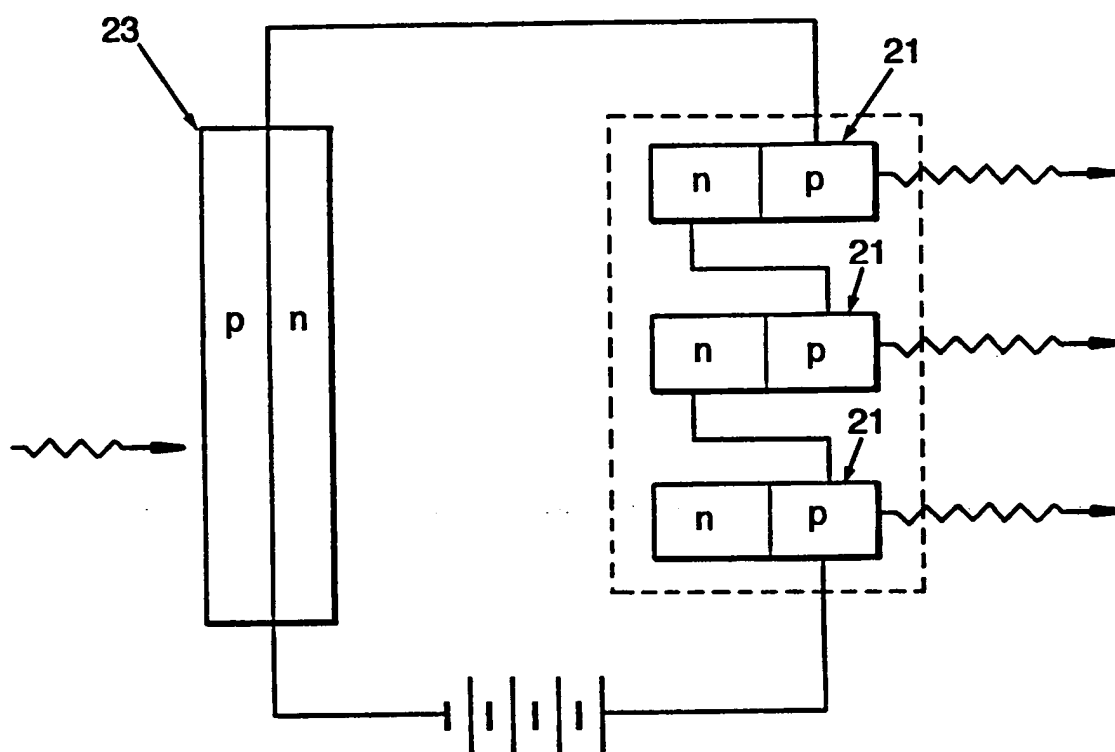


Fig.9.



INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 98/01775

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H01S3/25 H04B10/16 H03F3/08 H04B10/12 H04B10/14

According to International Patent Classification(IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01S H04B H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y A	WO 96 08857 A (EDWARDS PAUL JULIAN ;CHEUNG WOOD NANG (AU)) 21 March 1996 see the whole document	1-3,6-8, 10-12 1,14-18 4,5,9
X A	WO 95 07565 A (ERICSSON TELEFON AB L M) 16 March 1995 see page 3, line 9-28 see page 6, line 1-13 see page 8, line 1-9	1-5 10,14, 15,17
X A	WO 92 15136 A (UNIV MELBOURNE) 3 September 1992 see page 2	1-5 10,14, 15,17
	-/-	

☒ Further documents are listed in the continuation of box C.☒ Patent family members are listed in annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"Z" document member of the same patent family

Date of the actual completion of the international search

16 October 1998

Date of mailing of the international search report

30/10/1998

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax (+31-70) 340-3018

Authorized officer

Claessen, L

INTERNATIONAL SEARCH REPORT

Inter national Application No

PCT/GB 98/01775

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	PATENT ABSTRACTS OF JAPAN vol. 009, no. 015 (E-291), 22 January 1985 & JP 59 163927 A (SUMITOMO DENKI KOGYO KK), 17 September 1984 see abstract	1, 14, 15
A	PATENT ABSTRACTS OF JAPAN vol. 011, no. 382 (E-564), 12 December 1987 & JP 62 146024 A (SUMITOMO ELECTRIC IND LTD), 30 June 1987 see abstract	1, 14, 15
Y	US 4 644 587 A (TAKAHASHI YOSHIFUSA ET AL) 17 February 1987 see abstract; figure 5	1, 15-18
A	PATENT ABSTRACTS OF JAPAN vol. 014, no. 564 (E-1013), 14 December 1990 & JP 02 244834 A (FUJITSU LTD), 28 September 1990 see abstract	1, 15
A	US 4 212 023 A (CHEN ROBERT I ET AL) 8 July 1980 see the whole document	1, 10
A	US 4 278 515 A (PEARLSTEIN FRED ET AL) 14 July 1981 see the whole document	1, 10
A	US 5 278 515 A (MATHEWS KURK D) 11 January 1994 see the whole document	1
P, X	D.R. WIGHT ET AL: "Optically coupled transistors and the achievement of insertion gain in broadband directly modulated optical links" IEE COLLOQUIUM ON FIBRE OPTICS IN MICROWAVE SYSTEMS AND RADIO ACCES (REF. NO. 1997/131, 26 June 1997, page 5/1-5 XP002081085 birmingham uk see the whole document	1-18

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/GB 98/01775

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9608857 A	21-03-1996	AU 682982 B AU 3514495 A JP 10505201 T	23-10-1997 29-03-1996 19-05-1998
WO 9507565 A	16-03-1995	SE 501721 C CA 2169439 A EP 0717884 A JP 9502307 T SE 9302951 A US 5568498 A	02-05-1995 16-03-1995 26-06-1996 04-03-1997 11-03-1995 22-10-1996
WO 9215136 A	03-09-1992	AU 636792 B AT 137893 T AU 8343991 A CA 2103720 A DE 69210589 D DE 69210589 T EP 0571476 A JP 6505364 T US 5440577 A	06-05-1993 15-05-1996 20-08-1992 14-08-1992 13-06-1996 02-10-1996 01-12-1993 16-06-1994 08-08-1995
US 4644587 A	17-02-1987	JP 1299634 C JP 58096436 A JP 59016453 B DE 3244851 A GB 2111337 A,B	31-01-1986 08-06-1983 16-04-1984 16-06-1983 29-06-1983
US 4212023 A	08-07-1980	DE 2947267 A JP 1123785 C JP 55102281 A JP 57012546 B	04-06-1980 30-11-1982 05-08-1980 11-03-1982
US 4278515 A	14-07-1981	US 4365481 A	28-12-1982
US 5278515 A	11-01-1994	NONE	

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H01S3/25 H04B10/16 H03F3/08 H04B10/12 H04B10/14

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H01S H04B H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y A	WO 96 08857 A (EDWARDS PAUL JULIAN ;CHEUNG WOOD NANG (AU)) 21 March 1996 see the whole document	1-3, 6-8, 10-12 1, 14-18 4, 5, 9
X A	WO 95 07565 A (ERICSSON TELEFON AB L M) 16 March 1995 see page 3, line 9-28 see page 6, line 1-13 see page 8, line 1-9	1-5 10, 14, 15, 17
X A	WO 92 15136 A (UNIV MELBOURNE) 3 September 1992 see page 2	1-5 10, 14, 15, 17
	--- -/- ---	

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents:

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "B" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
- "&" document member of the same patent family

Date of the actual completion of the international search

16 October 1998

Date of mailing of the international search report

30/10/1998

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentaan 2
NL - 2280 HV Rijswijk
Tel: (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Claessen, L

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	PATENT ABSTRACTS OF JAPAN vol. 009, no. 015 (E-291), 22 January 1985 & JP 59 163927 A (SUMITOMO DENKI KOGYO KK), 17 September 1984 see abstract	1, 14, 15
A	PATENT ABSTRACTS OF JAPAN vol. 011, no. 382 (E-564), 12 December 1987 & JP 62 146024 A (SUMITOMO ELECTRIC IND LTD), 30 June 1987 see abstract	1, 14, 15
Y	US 4 644 587 A (TAKAHASHI YOSHIFUSA ET AL) 17 February 1987 see abstract; figure 5	1, 15-18
A	PATENT ABSTRACTS OF JAPAN vol. 014, no. 564 (E-1013), 14 December 1990 & JP 02 244834 A (FUJITSU LTD), 28 September 1990 see abstract	1, 15
A	US 4 212 023 A (CHEN ROBERT I ET AL) 8 July 1980 see the whole document	1, 10
A	US 4 278 515 A (PEARLSTEIN FRED ET AL) 14 July 1981 see the whole document	1, 10
A	US 5 278 515 A (MATHEWS KURK D) 11 January 1994 see the whole document	1
P, X	D.R. WIGHT ET AL: "Optically coupled transistors and the achievement of insertion gain in broadband directly modulated optical links" IEE COLLOQUIUM ON FIBRE OPTICS IN MICROWAVE SYSTEMS AND RADIO ACCES (REF. NO. 1997/131, 26 June 1997, page 5/1-5 XP002081085 birmingham uk see the whole document	1-18

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
WO 9608857	A	21-03-1996	AU 682982 B AU 3514495 A JP 10505201 T	23-10-1997 29-03-1996 19-05-1998
WO 9507565	A	16-03-1995	SE 501721 C CA 2169439 A EP 0717884 A JP 9502307 T SE 9302951 A US 5568498 A	02-05-1995 16-03-1995 26-06-1996 04-03-1997 11-03-1995 22-10-1996
WO 9215136	A	03-09-1992	AU 636792 B AT 137893 T AU 8343991 A CA 2103720 A DE 69210589 D DE 69210589 T EP 0571476 A JP 6505364 T US 5440577 A	06-05-1993 15-05-1996 20-08-1992 14-08-1992 13-06-1996 02-10-1996 01-12-1993 16-06-1994 08-08-1995
US 4644587	A	17-02-1987	JP 1299634 C JP 58096436 A JP 59016453 B DE 3244851 A GB 2111337 A, B	31-01-1986 08-06-1983 16-04-1984 16-06-1983 29-06-1983
US 4212023	A	08-07-1980	DE 2947267 A JP 1123785 C JP 55102281 A JP 57012546 B	04-06-1980 30-11-1982 05-08-1980 11-03-1982
US 4278515	A	14-07-1981	US 4365481 A	28-12-1982
US 5278515	A	11-01-1994	NONE	